

## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/849,102	05/04/2001	David Brewer	07316/055003 7965	
42178	7590 07/11/2005		EXAMINER	
EMULEX DESIGN & MANUFACTURING CORPORATION C/O MORRISON & FOERSTER LLP 555 WEST FIFTH STREET, SUITE 3500			WILSON, ROBERT W	
			ART UNIT	PAPER NUMBER
LOS ANGELES, CA 90013			2661	

DATE MAILED: 07/11/2005

Please find below and/or attached an Office communication concerning this application or proceeding.



			<b>,</b>			
	Applic	cation No.	Applicant(s)			
Office Action Summant		9,102	BREWER ET AL.			
Office Action Summar	Exam	iner	Art Unit			
		t W. Wilson	2661			
The MAILING DATE of this com Period for Reply	munication appears on	the cover sheet with the d	correspondence address			
A SHORTENED STATUTORY PERIOD THE MAILING DATE OF THIS COMM  - Extensions of time may be available under the provafter SIX (6) MONTHS from the mailing date of this if the period for reply specified above is less than the If NO period for reply is specified above, the maxim  - Failure to reply within the set or extended period for Any reply received by the Office later than three may be arrived patent term adjustment. See 37 CFR 1.704	MUNICATION. risions of 37 CFR 1.136(a). In n communication. rirty (30) days, a reply within the rum statutory period will apply a reply will, by statute, cause the onths after the mailing date of th	o event, however, may a reply be ting estatutory minimum of thirty (30) day and will expire SIX (6) MONTHS from a application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).			
Status						
1) Responsive to communication (s	s) filed on <u>16 June 200</u>	<u>95</u> .				
2a) ☐ This action is <b>FINAL</b> .	This action is <b>FINAL</b> . 2b)⊠ This action is non-final.					
•	Since this application is in condition for allowance except for formal matters, prosecution as to the ments is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) ⊠ Claim(s) 1 is/are pending in the 4a) Of the above claim(s)  5) □ Claim(s) is/are allowed.  6) ⊠ Claim(s) 1 is/are rejected.  7) □ Claim(s) is/are objected.  8) □ Claim(s) are subject to re	is/are withdrawn from					
Application Papers						
9)☐ The specification is objected to I	y the Examiner.					
	☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.					
Applicant may not request that any	•	•	• •			
Replacement drawing sheet(s) incl						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  a) All b) Some * c) None of:  1. Certified copies of the priority documents have been received.  2. Certified copies of the priority documents have been received in Application No  3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  * See the attached detailed Office action for a list of the certified copies not received.						
American (a)						
Attachment(s)  1)  Notice of References Cited (PTO-892)		·4)  Interview Summary	(PTO-413)			
2) D Notice of Draftsperson's Patent Drawing Revi		Paper No(s)/Mail D	ate			
<ol> <li>Information Disclosure Statement(s) (PTO-14 Paper No(s)/Mail Date</li> </ol>	49 or PTO/SB/08)	5)	Patent Application (PTO-152)			

Application/Control Number: 09/849,102 Page 2

Art Unit: 2661

## **Double Patenting**

The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

2. Claim 1 is rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claim 1 of U.S. Patent No. 6,101,166. Although the conflicting claims are not identical, they are not patentably distinct from each other because:

Referring to claim 1, claim 1 of U.S. Patent No.: 6,101,166 teaches: A hub port which detects loop failure initialization which means that the hub port has a error detecting portion. The hub port receives the loop failure initialization from a faulty node. The hub port is connected in a loop and bypasses the faulty node upon receiving the loop failure initialization.

Claim 1 of U.S. Patent No.: 6,101,166 does not expressly call for: a plurality of hub ports or loop initialization detect primitive (L8) but teaches a plurality of node ports and loop failure initialization detect.

It is within the level of one skilled in the art to implement a plurality of hub ports in order to support a plurality of node ports and to implement the loop initialization as a software primitive which the implementer decided to name LIP 8.

## Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Art Unit: 2661

DeFoster et. al.; US Patent No.: 5,754,549 dated 5/19/98 in which he discloses a loop initialization primitive upon initialization of a loop but does not teach utilization of the primitive upon detection of a failure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Robert W. Wilson whose telephone number is 571/272-3075. The examiner can normally be reached on M-F (8:00-4:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chau T. Nguyen can be reached on 571/272-3126. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Robert W Wilson

Examiner

Art Unit 2661

RWW 6/30/05 BOB PHUNKULH PRIMARY EXAMINER